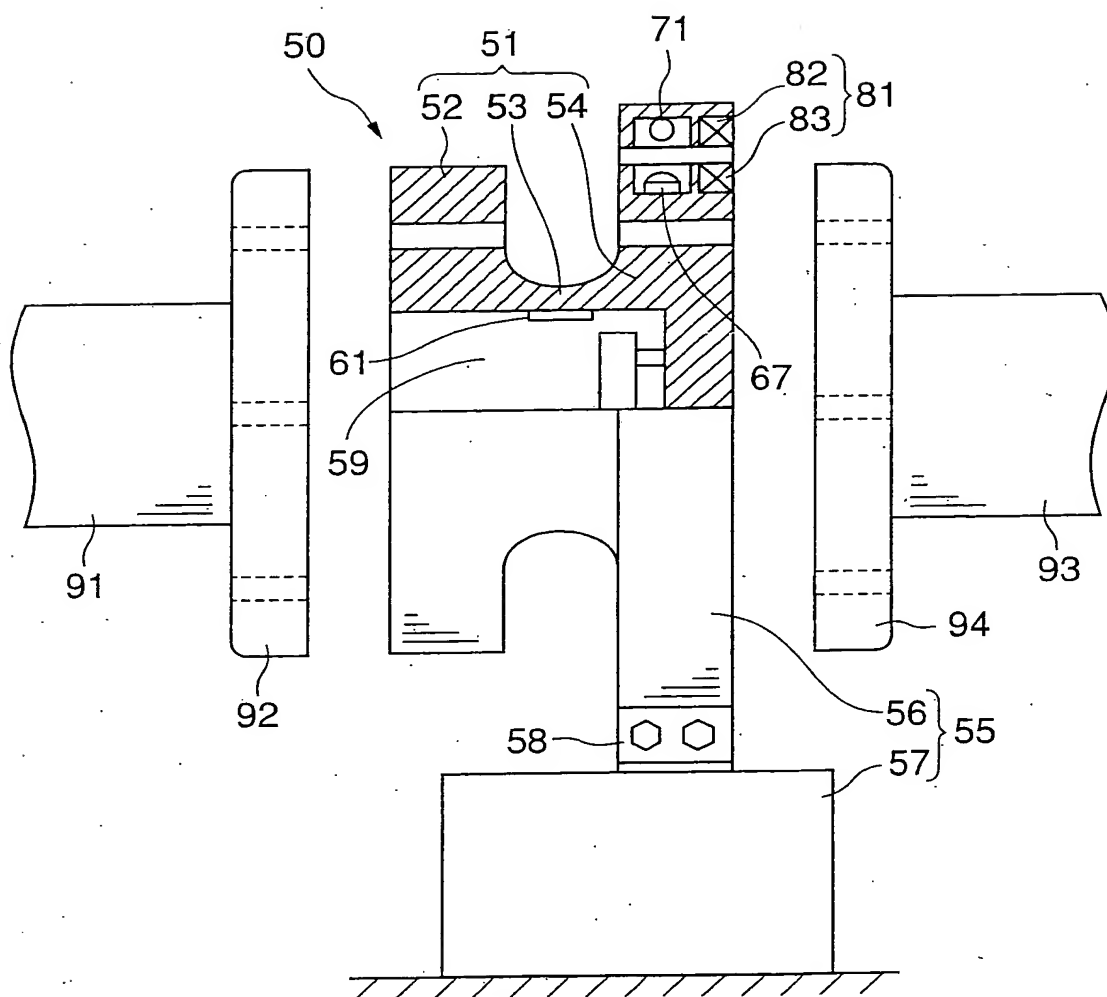
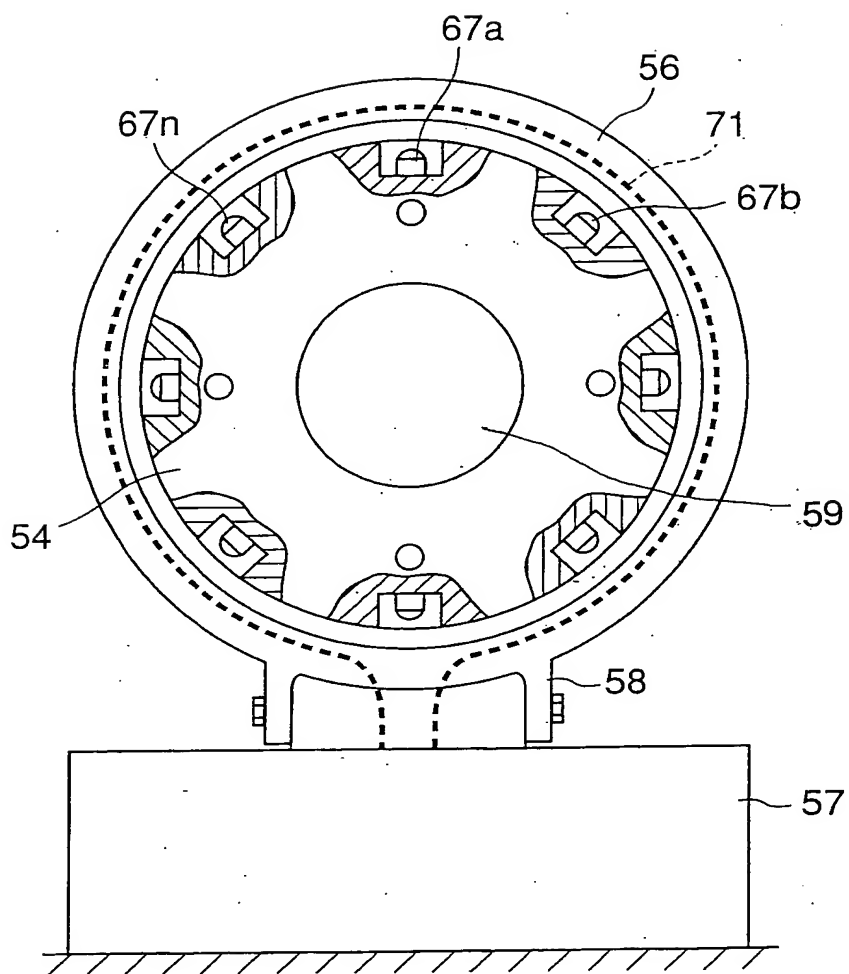


FIG. 1



PRIOR ART

FIG. 2



PRIOR ART

A detailed cross-sectional view of a semiconductor device assembly, labeled as FIG. 10. The diagram shows a central substrate 10 with various layers and components. A top layer 11 contains regions 12, 13, and 14. Below this, there are layers 12a and 14a. A central vertical feature 19 is surrounded by structures 21 and 27. At the bottom, there is a large rectangular block 15 containing a cavity 18a, 18b. To the left and right of the central structure are vertical bars 92 and 94, respectively, which are part of a larger assembly 91 and 93. Various other labels like 16, 17, 18b, 28, 41, and 43 indicate specific features and interfaces.

FIG. 4

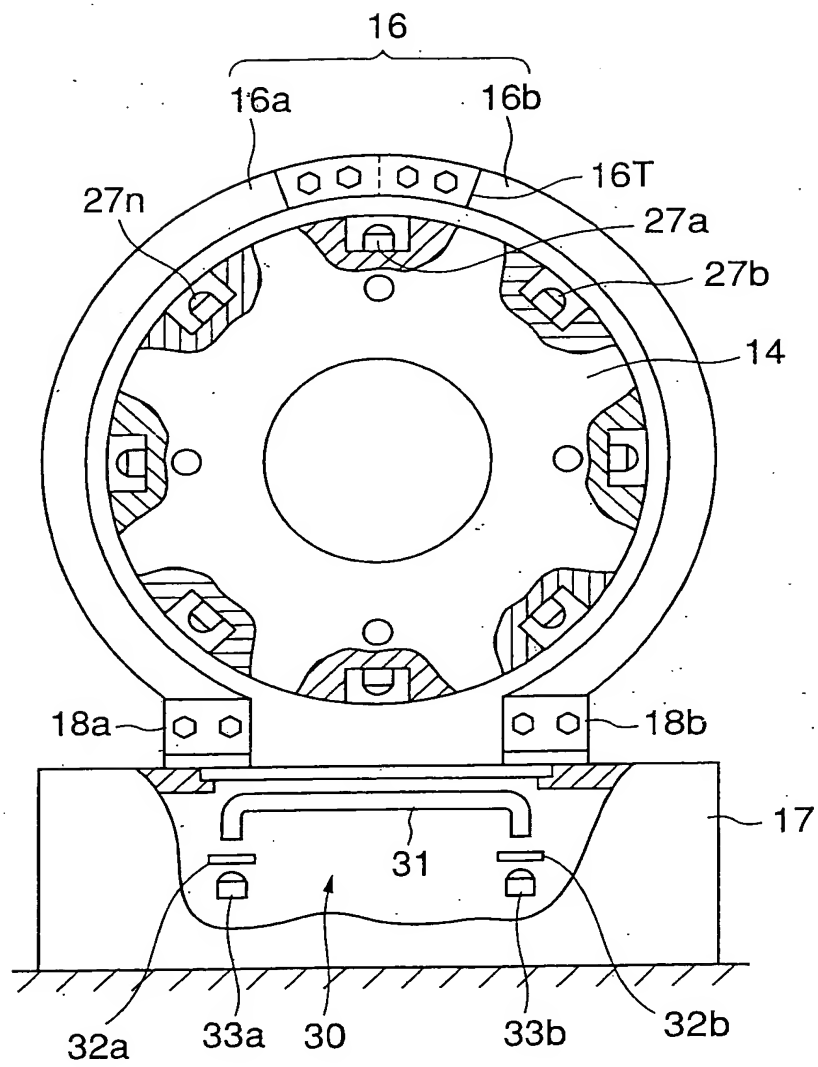


FIG. 5A

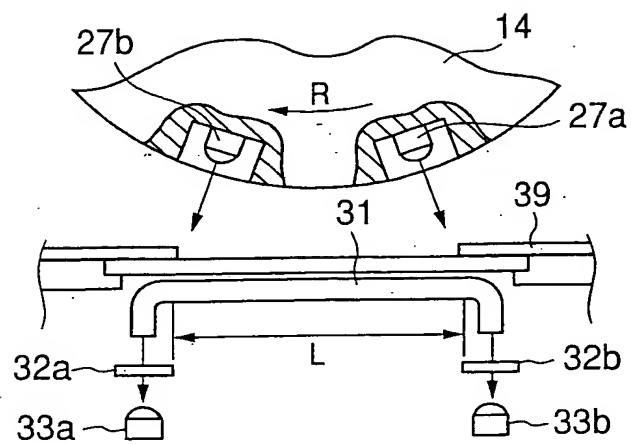


FIG. 5B

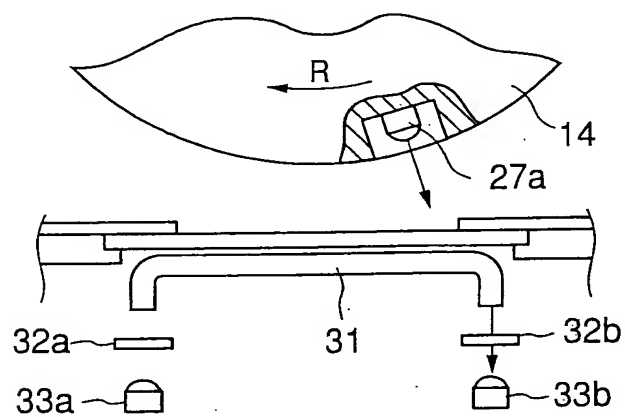


FIG. 5C

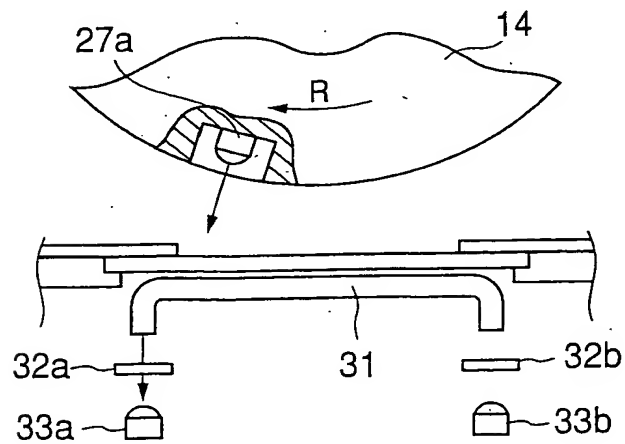


FIG. 6

